SPECIFICATION AMENDMENTS

Page 21, lines 8-15, please amend as follows:

The sampling phase P is supplied to the sample and hold

element 14 and, together with the parameters k_0 [[ad]] and

k_2, determines the time at which a data symbol value or bit

7 from a particular register cell in the shift register 2

needs to be read [[7]]. The value read is respectively

held for a symbol duration of $T_{Bit} = 9 \cdot T_S$, and in this way

the sampled input signal $RXDA_{sampled}$ is produced. This signal

can be supplied to a microcontroller μC via a data-

conversion unit 15, for example.